

L Number	Hits	Search Text	DB	Time stamp
-	926	CMOS with ESD	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/02 20:22
-	2	("5,287,241").PN.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/02 20:26
-	0	("performancesameprotection").PN.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/02 20:26
-	13919	performance same protection	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/02 20:28
-	754	esd and (performance same protection)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/02 20:27
-	415	cmos and (esd and (performance same protection))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/02 20:27
-	509	performance adj3 circuit and protection adj3 circuit	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/02 20:28
-	87	esd and cmos and (performance adj3 circuit and protection adj3 circuit)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/02 20:28

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